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PATENT NUMBER

U.S. UTILITY Patent Application

CO.I.P.E**PATENT DATE**

SCANNED

APPLICATION NO. 09/890226	CONT/PRIOR D F	CLASS 257	SUBCLASS	ART UNIT 2811	EXAMINER <u>G. J. ...</u>
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APPLICANTS

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Eric Daniel


Title

Integrated circuit device, electronic module for chip cards using said device and method for making same

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)							
CLASS		SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION										

 TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
				ISSUE BATCH NUMBER	
	_____ (Legal Instruments Examiner) (Date)				

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